

Figure 16. Transfer Characteristics and Equations for Figure 15

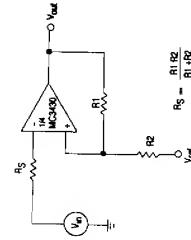


Figure 17. Double-Ended Limit Detector

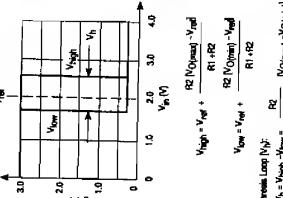
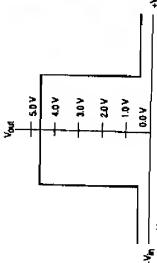
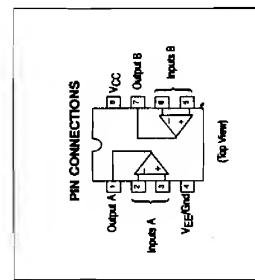
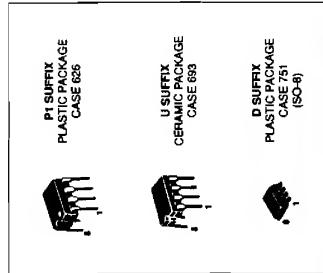
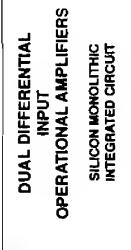


Figure 18. Voltage Transfer Function



dual, Low Power
Operational Amplifiers

- The negative power supply voltage.
- Short Circuit Protected Outputs
- True Differential Input Stage
- Single Supply Operation: 3.0 V to 36 V
- Low Input Bias Currents
- Internally Compensated
- Common Mode Range Extends to Negative
- Class AB Output Stage for Minimum CROS
- Single and Split Supply Operations Available



ORDERING INFORMATION					
Device	Temperature Range		Package		
INC358P1	-40°	to -85°C	Plastic DIP		
INC358P1	0°	to +70°C	Plastic DIP		
INC358P1	+70°	to +70°C	Ceramic DIP		
INC358J1	-55°	to +125°C	Ceramic DIP		
INC358J1	-55°	to +125°C	Ceramic DIP		

NOTES: 1. Split Power Supplies.
2. For supply voltages

1. Spin Power Suppliers.
2. For supply voltages less than ± 18 V, the absolute maximum input voltage

MC3458, MC3558, MC3358

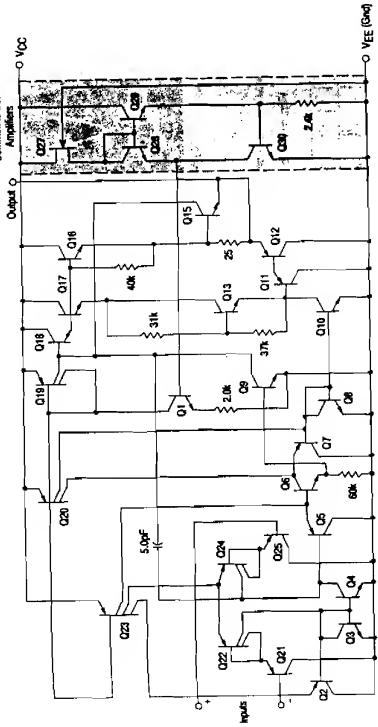
ELECTRICAL CHARACTERISTICS (For MC3558, MC3458, V_{CC} = +15 V, V_{EE} = -15 V, T_A = 25°C, unless otherwise noted.)

(For MC3358, $V_{CC} = +14$ V, $V_{EE} = \text{Gnd}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

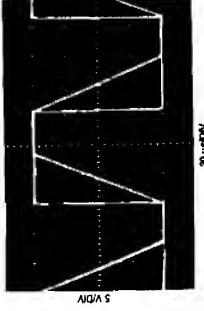
Table 3: T_{onset} = -55°C for MC3656, 0°C for MC3455, -40°C for MC3555

Representative Circuit Schematics

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Inverter Pulse Response



CIRCUIT DESCRIPTION

The MC2538 Series is made using two internally compensated, two-stage operational amplifiers. The first stage consists of differential input devices C24 and C22, with input B_1 buffers. C25 and C21 are the differential-to-single ended converter C3 and C4. The first stage performs not only the first stage gain function but also performs the level shifting and transconductance reduction functions. By reducing the transconductance, a smaller compensation capacitor (0.5 pF) can be employed, thus saving chip area. The transconductance reduction is accomplished by splitting the collectors of C24 and C22. Another feature of this input stage is that the Input Common-Mode range can include the negative supply or ground. In the second stage the differential supply operation, without saturating either the input devices or the differential or single-ended converter, the output devices consist of a standard current source load amplifier stage.

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ESTATE PLANNING AND INVESTMENT STRATEGIES

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MC3458, MC3558, MC3358

MC3458, MC3558, MC3358

Figure 1. Sine Wave Response

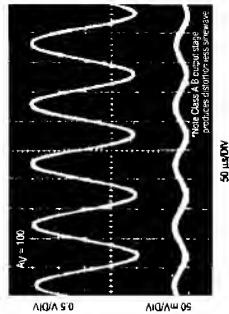


Figure 2. Open-Loop Frequency Response

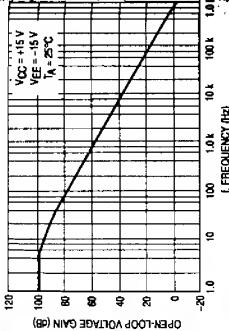


Figure 7. Voltage Reference

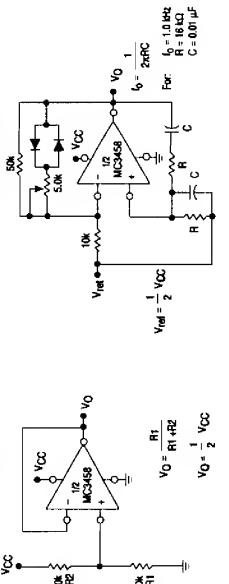


Figure 8. Wien Bridge Oscillator

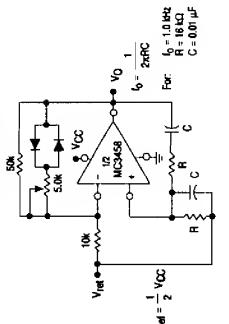


Figure 3. Power Bandwidth

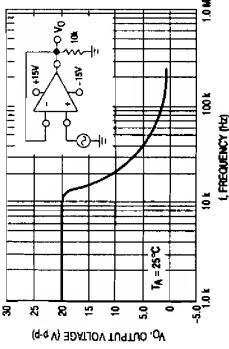


Figure 4. Output Swing versus Supply Voltage

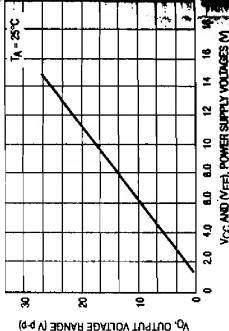


Figure 9. High Impedance Differential Amplifier

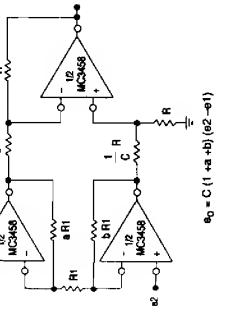


Figure 10. Comparator with Hysteresis

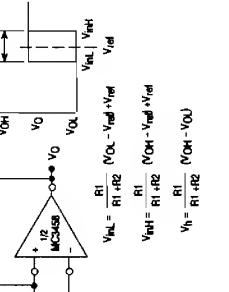


Figure 5. Input Bias Current versus Temperature

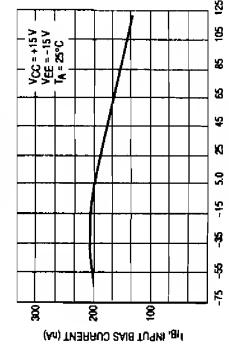


Figure 6. Input Bias Current versus Supply Voltage

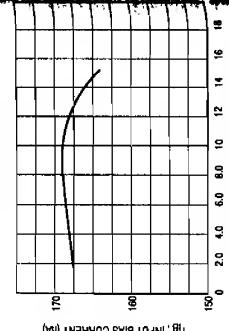


Figure 7. Input Bias Current versus Frequency

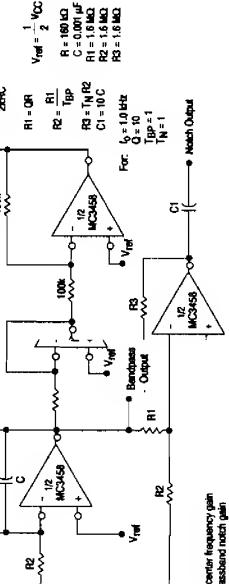


Figure 8. Filter

